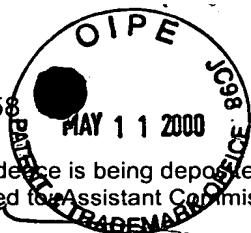


Docket No.: GR 99 P 1058



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By: 

Date: May 5, 2000

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CFR

6/14/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wilfried Daehn
Appl. No. : 09/484,781
Filed : January 18, 2000
Title : Integrated Semiconductor Circuit and Method for Functional Testing of Pad Cells
Art Unit : 2812

LETTER

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

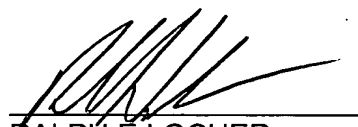
Undersigned counsel has received the Filing Receipt for the above-identified application.

However, the attorney docket number has been omitted and should be listed as:

GR 99 P 1058

It is respectfully requested that the Patent Office Records be changed and that a new Filing Receipt be issued, so that the printed patent will show the correct attorney docket number.

Respectfully submitted,



RALPH E. LOCHER
REG NO. 41,947

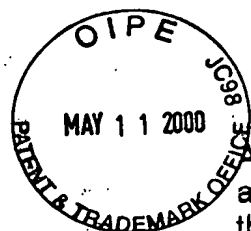
/av

Date: May 5, 2000

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Hollywood, FL 33022-2480
Tel: (954) 925-1100
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COMBINED DECLARATION AND POWER OF ATTORNEY IN ORIGINAL APPLICATION

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

INTEGRATED SEMICONDUCTOR CIRCUIT AND METHOD FOR FUNCTIONAL TESTING OF PAD CELLS

described and claimed in the specification bearing that title, that I understand the content of the specification, that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve month prior to this application, that I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application under 37 C.F.R. 1.56a, and that no application for patent or inventor's certificate of this invention has been filed earlier than the following in any country foreign to the United States prior to this application by me or my legal representatives or assigns:

German Application No. 199 01 480.4, filed January 15, 1999, the International Priority of which is claimed under 35 U.S.C. §119.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

HERBERT L. LERNER (Reg.No.20,435)
LAURENCE A. GREENBERG (Reg.No.29,308)
WERNER H. STEMER (Reg.No.34,956)
RALPH E. LOCHER (Reg.No. 41,947)

Address all correspondence and telephone calls to:

LERNER AND GREENBERG, P.A.
POST OFFICE BOX 2480
HOLLYWOOD, FLORIDA 33022-2480
TEL: (954) 925-1100
FAX: (954) 925-1101

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/484,781	01/18/2000	2812	820	629921058	2	10	1

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Applicant(s)

WILFRIED DAEHN, CELLE, GERMANY;

Continuing Data as Claimed by Applicant**Foreign Applications**

GERMANY 199 01 460.4 01/15/1999

If Required, Foreign Filing License Granted 03/13/2000

**

TitleINTEGRATED SEMICONDUCTOR CIRCUIT AND METHOD FOR FUNCTIONAL TESTING OF
PAD CELLS**Preliminary Class**

438

Data entry by : HARRISON, BRUCE

Team : OIPE

Date: 04/25/2000

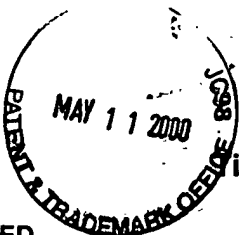
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Title 35, United States Code, Section 184
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